

Notice of References Cited	Application/Control No. 10/620,045		Applicant(s)/Patent Under Reexamination BHATTACHARYA ET AL.	
	Examiner Mary C. Jacob		Art Unit 2123	Page 1 of 2

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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*	B	US-2002/0186245 A1	12-2002	Chandhoke et al.	345/764
*	C	US-2003/0038842 A1	02-2003	Peck et al.	345/763
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	V	Ishida et al "A 10-GHz 8-b Multiplexer/Demultiplexer Chip Set for the SONET STS-192 System", IEEE Journal of Solid-State Circuits, Vol. 26, No. 12, December 1991				
	W	Sun et al "Simulation Studies of Multiplexing and Demultiplexing Performance in ATM Switch Fabrics", Performance Engineering in Telecommunications Network Teletraffic Symposium, 14-16 Apr 1993, pages 21/1 - 21/5				
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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